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Application Number:	09505949			
International Application Number:	Method and apparatus for achieving architectural correctness in a multi-mode processor providing floating-point support Michael Chow Thomas M Coester Blakely Sokoloff Taylor and Zafman LLP 12400 Wilshire Boulevard Seventh Floor			
Confirmation Number:	5605			
Title of Invention:	Method and apparatus for achieving architectural correctness in a multi-mode processor providing floating-point support			
First Named Inventor/Applicant Name:	Michael Chow			
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Filer:	Joseph Lutz/Suzanne Johnston			
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)		
1		42P6447_Response_041307 .pdf	655200	yes	16		
	Multipart Description/PDF files in .zip description						
	Document Description		Start	End			
	Amendment - After Non-Final Rejection		1	1			
	Claims		2	5			
	Applicant Arguments/Remarks Made in an Amendment		6	16			
Warnings:							
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New International Application Filed with the USPTO as a Receiving Office

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